

## WEST Search History





DATE: Tuesday, May 03, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L21	L20 same (instruct\$4 or command)	2
<input type="checkbox"/>	L20	L19 same (power\$4 near2 down) same (power\$4 near2 up)	40
<input type="checkbox"/>	L19	(plurality or multiple or individual or separate or independent) with L18	33619
<input type="checkbox"/>	L18	(power near2 (supply or source)) with control\$4	360666
<input type="checkbox"/>	L17	L15 same network\$4	3
<input type="checkbox"/>	L16	L15 with network\$4	0
<input type="checkbox"/>	L15	(instruct\$4 or command\$4) with (power\$4 near2 (down or reduc\$7 or lower\$4) near2 computer)	126
<input type="checkbox"/>	L14	L13 with network\$4	126
<input type="checkbox"/>	L13	(instruct\$4 or command\$4) with (power\$4 near2 (down or reduc\$7 or lower\$4))	5939
	<i>DB=EPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L12	L11	23
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L11	user with message with power\$4	1870
<input type="checkbox"/>	L10	L9.ab.	0
	<i>DB=EPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L9	L8	0
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L8	((power near2 down) with sequence with delay\$4)	47
<input type="checkbox"/>	L7	((power near2 down) with sequence with delay\$4) same network same (user with message)	0
<input type="checkbox"/>	L6	(power near2 remov\$4) same network same (user with message)	5
<input type="checkbox"/>	L5	(power near2 remov\$4) with network with user with message	0
	<i>DB=EPAB; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L4	"EP 0499564 A2"	0
<input type="checkbox"/>	L3	"EP 0 499 564 A2"	0
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<input type="checkbox"/>	L2	"0 499 564 A2"	0
<input type="checkbox"/>	L1	"0499564A2"	0

END OF SEARCH HISTORY

[First Hit](#) [Fwd Refs](#) [Previous Doc](#) [Next Doc](#) [Go to Doc#](#)

Generate Collection

Print

L8: Entry 20 of 47

File: USPT

Aug 15, 2000

US-PAT-NO: 6104582

DOCUMENT-IDENTIFIER: US 6104582 A

TITLE: Storm alert automatic system power-down

DATE-ISSUED: August 15, 2000

## INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Cannon; Joseph M.	Harleysville	PA		
Johanson; James A.	Macungie	PA		

US-CL-CURRENT: [361/1](#); [361/111](#), [361/118](#), [361/119](#)

## ABSTRACT:

Apparatus and processes to automatically power-down a computer system upon determination of the presence, imminence, or forecast of severe weather in the local area. A computer system receives data relating to a weather forecast for a particular local area from a weather service communication server and, based on a determination of the presence, imminence or forecast of severe weather in the local area, initiates an automatic power-down sequence. A delay may be provided to allow the user time to abort the initiation of the automatic power-down sequence. The data may be transmitted in any suitable manner, e.g., over a telephone line through the PSTN, using the Internet, using a paging system, etc. The weather service communication server is provided with the particular local area relating to the computer system. For instance, in the case of a dial-up telephone call to the weather service communication server, call related information such as the telephone exchange number and/or the area code in Caller ID information can be used by the weather service communication server to determine the local area relating to the particular computer system. Thus, a user is provided additional security against damage which may be caused to all or a portion of a computer system due to lightning strikes, downed telephone poles, etc. as a result of severe weather in a local area.

26 Claims, 4 Drawing figures

Exemplary Claim Number: 1

Number of Drawing Sheets: 4

[Previous Doc](#) [Next Doc](#) [Go to Doc#](#)

[First Hit](#)[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

Generate Collection

Print

L21: Entry 1 of 2

File: PGPB

Nov 18, 2004

DOCUMENT-IDENTIFIER: US 20040227404 A1

TITLE: Controlling devices using cascaded control units

Detail Description Paragraph:

[0048] In operation of the PSCU 10, information including commands, acknowledgements, and status signals is communicated between the primary and secondary control units 11 and 12 in order to establish and maintain their synchronism, as described below with reference to FIGS. 5 and 6. This information, and the configuration of the PSCU 10 as described above, are sufficient for up to six CSMS and their controlled power supplies. In order to accommodate a greater number of CSMS and controlled power supplies, this invention provides a cascade arrangement which permits a plurality of PSCUs to be used in cooperation with one another, while maintaining desired sequence topologies for power-up and shut-down of the controlled power supplies.

[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

[First Hit](#)[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

Generate Collection

Print

L20: Entry 8 of 40

File: PGPB

Nov 4, 2004

DOCUMENT-IDENTIFIER: US 20040217749 A1

TITLE: POWER SUPPLY CONTROLLER

Summary of Invention Paragraph:

[0021] A further aspect of the invention provides a power supply controller for controlling a plurality of isolating power supplies, each for converting an input voltage to one or more respective output voltages, for power-up or shut-down in a desired sequence, the power supply controller comprising: a first control unit powered from the input voltage and having outputs for enabling the isolating power supplies via an input on a primary side of each isolating power supply, the first control unit being arranged to monitor the input voltage; a second control unit powered from the input voltage in an isolated manner and arranged to monitor the output voltage of each isolating power supply; and an isolating signal coupler between the first and second control units; the control units together being responsive to the monitored input voltage and the monitored output voltages so that the first control unit enables the isolating power supplies in accordance with the desired power-up or shut-down sequence.

## CLAIMS:

18. A power supply controller for controlling a plurality of isolating power supplies, each for converting an input voltage to one or more respective output voltages, for power-up or shut-down in a desired sequence, the power supply controller comprising: a first control unit powered from the input voltage and having outputs for enabling the isolating power supplies via an input on a primary side of each isolating power supply, the first control unit being arranged to monitor the input voltage; a second control unit powered from the input voltage in an isolated manner and arranged to monitor the output voltage of each isolating power supply; and an isolating signal coupler between the first and second control units; the control units together being responsive to the monitored input voltage and the monitored output voltages so that the first control unit enables the isolating power supplies in accordance with the desired power-up or shut-down sequence.

[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

[First Hit](#)[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

Generate Collection

Print

L20: Entry 12 of 40

File: PGPB

May 9, 2002

DOCUMENT-IDENTIFIER: US 20020054517 A1

TITLE: Sequence circuit and semiconductor device using sequence circuit

Summary of Invention Paragraph:

[0004] A semiconductor device using a layer-built cell capacitor and a negative word-line resetting system generates a plurality of power sources from an internal step-down power source, and uses the plurality of power sources, for example. A sequence circuit shown in FIG. 1 controls start up of a power-supply circuit generating the plurality of power sources from the internal step-down power source.

Detail Description Paragraph:

[0034] The sequence circuit 20 includes a Vii voltage-detecting unit 21, a Vbl/Vcp voltage-detecting unit 23, a Vnn clamp unit 24 and a sequencer 26. The Vii voltage-detecting unit 21 detects an internal step-down power-supply voltage Vii. The Vbl/Vcp voltage-detecting unit 23 detects the bit-line power-supply voltage Vbl and the cell-plate power-supply voltage Vcp. The Vnn clamp unit 24 clamps the negative power-supply voltage Vnn to a ground voltage Vss. Additionally, the sequencer 26 controls a start-up order of power-supply voltage-generating units such as the Vbl/Vcp generating unit 22 and the Vnn generating unit 25. The sequencer 26 includes one or a plurality of sequencers 26-1, 26-2 and so on, in accordance with the number of the power-supply voltage-generating units that controls the start-up order.

[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

[First Hit](#) [Fwd Refs](#)[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)

Generate Collection

Print

L20: Entry 20 of 40

File: USPT

Mar 18, 1997

DOCUMENT-IDENTIFIER: US 5613095 A

**\*\* See image for Certificate of Correction \*\***

TITLE: Peripheral card having independent functionally and method used therewith

Detailed Description Text (2):

Generally this disclosure deals with a peripheral arrangement for a host computer. The peripheral arrangement includes a peripheral card that has a PCMCIA compliant interface that may be generally and advantageously employed when, among others, the peripheral arrangement includes independent functionality, such as may be present if a separate power supply and controller (CPU) is included with the peripheral arrangement. The preferred embodiment is a peripheral card that exhibits an independent and dependent operating state and has an interface function that is coupled to the peripheral card. The interface function is arranged and constructed to couple the peripheral card to the host computer over the PCMCIA compliant interface and initiate a dependent operating state at the peripheral card when inserted in a host computer that is or is subsequently powered up. The interface function that is coupled to the peripheral card during the dependent operating state, initiates an independent operating state when the peripheral card is removed or the host computer is powered down.

[Previous Doc](#)[Next Doc](#)[Go to Doc#](#)